**Concept paper of Masterthesis - Stand 01.04.2019**

**„Setup a Lane keeping assistance system evaluation demonstration using NXP SBC-S32V234 Evaluation Board and Simulator “**

**1.Background:**

The main objective of this master thesis is toSetup a Lane keeping assistance system evaluation demonstration using NXP SBC-S32V234 Evaluation Board and Simulator. Three cameras will be used to provide front view,dashboard view and right side lane view. The fusion of the camera data will assist in the Lane keeping assistance evaluation. The topics which will be discussed throughout this concept paper of master’s thesis are:Goal of the project, Projectstructure, Overview of software and Hardware, Projectplan, lessons learned and work progress.

**2.Goal:**

1.At first the NXP hardware will be evaluated for the autonomous validation Features e.g. LKAS.

2.The scenario on the simulator will be created and stream the video and environment parameterse.g.CARMAKER, VTD.

3.Camera setup in front of monitor with necessary covering so that we images of less noise.

4.Required Hardware setup(cameras will be focused to 3 simulator display which will display left side view, right side view and front view of lane.

5.At least 3 cameras shouldbeconnected with Board to provide 3 different video data which are left side view, front side view, dashboard view.

6.The board should provide 1 CAN Bus data and 1 Ethernet data and at least 3 camera inputs.

7.The camera images will be grabbed into the board via 3 cameras.

8.Image processing and calculation of required parameters will be done on NXP Boards.

9.The processed image and required parameters will be sent to simulator via CAN Bus or Ethernet Bus.

10.The calculated data will be compared with ground truth simulated environment.

11.Feedback is communicated to the processing unit for fine tuning the processing algorithm.

**3.Project Structure**

Stream the video and environment parameters from simulator

Display the Simulator video on three monitor

Setup the cameras in front of monitor with necessary covering

Hardware setup(three cameras will be focused to three simulator display which will display dashboard view,front view and right side lane view)

Grab the Simulator display images into nxp board via cameras

Image processing algorithm and required parameters calculation algorithm on nxp board

Send the processsed image to display for debugging

Calculate the required parameters and send to the simulator via CAN Bus or Ethernet

Comparison of calculated data with ground truth simulated environment on host pc or nxp

Feedback is comunicated to the processing unit (NXP or host pc) for fine tuning the algorithm.

KPI is evaluated

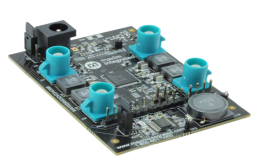
**4.Project Milestone:**

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| Project Name:Setup a Lane keeping assistance system evaluation demonstration using NXP SBC-S32V234 Evaluation Board | | | | | | | | | | | | | | | | | | | | | Date:24.05.19 | | | | |
| Year | 2019 | | | | | | | | | | | | | | | | | | | | | | | | |
| Month | April | | | | May | | | | June | | | | July | | | | August | | | | | September | | | |
| Week | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | | 21 | 22 | 23 | 24 |
| S32V234 Board Evaluation |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |  |  |  |  |
| S32DS Software Installation |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |  |  |  |  |
| Running Demo project on Emulator |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |  |  |  |  |
| Preparation of SD Card |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |  |  |  |  |
| Creation of own Demo Project |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |  |  |  |  |
| Camera setup |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |  |  |  |  |
| Analyzing Vision SDK architecture and change in required programming |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |  |  |  |  |
| Image processing and calculation of required parameters on NXP Board |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |  |  |  |  |
| Sending Image and required parameters to Simulator via CAN bus and Ethernet Bus |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |  |  |  |  |
| Comparison of calculated data with ground truth Simulated Environment |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |  |  |  |  |
| Probable errors fine tuning |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |  |  |  |  |
| Documentation and writing thesis report |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |  |  |  |  |

**5.Cost Analysis:**

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|  |  | Cost in € | | | | |
| Quantity | Work packages | Preparation | Measurement Setup | Execution | Report | **Total** |
| 1 | NXP SBC-S32V234 Board |  |  |  |  | **626.290**€ |
| 8 | MXOV10635-S32V Camera |  |  |  |  | **1158**€ |
| 3 | MAXIM deserializer |  |  |  |  | **106.150**€ |
| 4 | Monitors(3) |  |  |  |  | **450**€ **approx.** |

**6.Overview of the NXP Board,software and other components[1]:**



Simulator monitor OV10635 Camera Deserializer



NXP SBC-S32v234

Figure 1:NXP Board and other components

The SBC-S32V234 is a low-cost development platform for the S32V2 vision processor. The Module board contains S32V234 processor, memories and power regulators for the module board and the Carrier board contains all system specific i/o like camera connectors, ethernet port, display port, SD card slot, CAN ports etc.Total 8 cameras can be connected to the Board through deserializer.In this project MXOV10635-S32V camera have been used. The deserializer board model is MAX9286S32V234.S32DS 2018.R1 for Vision(Contains eclipse neon 4.6 framework) IDE from NXP will be used for simulation.

**7.Lesson learned and Work progress:**

At first S32DS 2018.R1 software were installed on Ubuntu 18.4.But S32 DS for Vision only supports Ubuntu 16.4 and openjdk-8.After installing Ubuntu 16.4 and openjdk-8 the required software has been installed successfully.From APEX graph project APEX program project and APEX application project is created accordingly using S32DS software on Linux.ISP dataflow project and ISP application project is also created using S32DS for vision software.The VSDK architecture is analyzed.The new APEX2 kernel project is created.Theupsampling and downsampling of image is done using own developed kernel and prebuilt kernel.The RGB image converted to Grayscale image using APEX core framework.The frame output buffer configuration is also done to display larger image

Hardware Setup:

1. On S32-SBC PWA jumper connected to 12V power to power deserializer board.

2. On S32-SBC the MAX deserializer board is connected to the MIPI-A.

3. On MAX deserializer board jumper JU4 is set to power the camera from SBC board (After setting the jumper the yellow light of each cameras are lit).

The full hardware setup image is as follows:



Figure2: Hardware Setup

Software Setup**:**

The SD card is prepared with SDK\_S32V2\_RTM\_1\_3\_0\_img\_yocto.tar.gz.After preparing the sd card it is inserted to the evaluation board. Camera captured images will be displayed on the HDMI connected display unit.Power on the boot, login and change directory to the demo folder. Then the sample application isp\_ov10635\_quad.elf was running on the board using the command

*root@s32v234sbc:~/vsdk# ./**isp\_ov10635\_quad.elf*

**Challenges:**The first challenge was to setup the environment variables on linux operating system to install S32 DS for Vision 2018.R1.Previously ubuntu 18 and openjdk-10 was installed on our linux operating pc.But S32 DS for Vision 2018.R1 only supports Ubuntu-16 and openjdk-8.After installing openjdk-8 and ubuntu-16 the software was working perfectly.At first SDK\_S32V2\_RTM\_1\_0\_0\_img\_yocto.tar.gz was used to prepare the sdcard.After that we came to know that the SBC-S32V234 board only support the below BSP file SDK\_S32V2\_RTM\_1\_3\_0\_img\_yocto.tar.gz.To provide 12V power to each camera from the deserealizer we need to set board jumber JU4.Since there are no proper documents which describes the kernel graph project,it took a little bit more time to learn more about Vision SDK architecture.

**Reference:**

[1].<https://www.nxp.com/support/developer-resources/evaluation-and-development-boards/ultra-reliable-dev-platforms/s32v-mpus-platforms/s32v-vision-and-sensor-fusion-evaluation-board:SBC-S32V234>